

Electronic Acknowledgement Receipt

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Application Number:	10700136
International Application Number:	
Confirmation Number:	3722
Title of Invention:	WIRING STRUCTURE ON SEMICONDUCTOR SUBSTRATE AND METHOD OF FABRICATING THE SAME
First Named Inventor/Applicant Name:	Tomio Matsuzaki
Customer Number:	1933
Filer:	Douglas Holtz/JD Dholakia
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement (IDS) Filed	03663_ids4.pdf	41651	no	3
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
2	Request for Continued Examination (RCE)	03663_rce2.pdf	642215	no	3
Warnings:					
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3	NPL Documents	324973.pdf	100878	no	4
Warnings:					
Information:					
4	Foreign Reference	228423.pdf	671168	no	7
Warnings:					
Information:					
5	Foreign Reference	135385.pdf	331590	no	4
Warnings:					
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6	Fee Worksheet (PTO-06)	fee-info.pdf	8213	no	2
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Total Files Size (in bytes):			1795715		

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